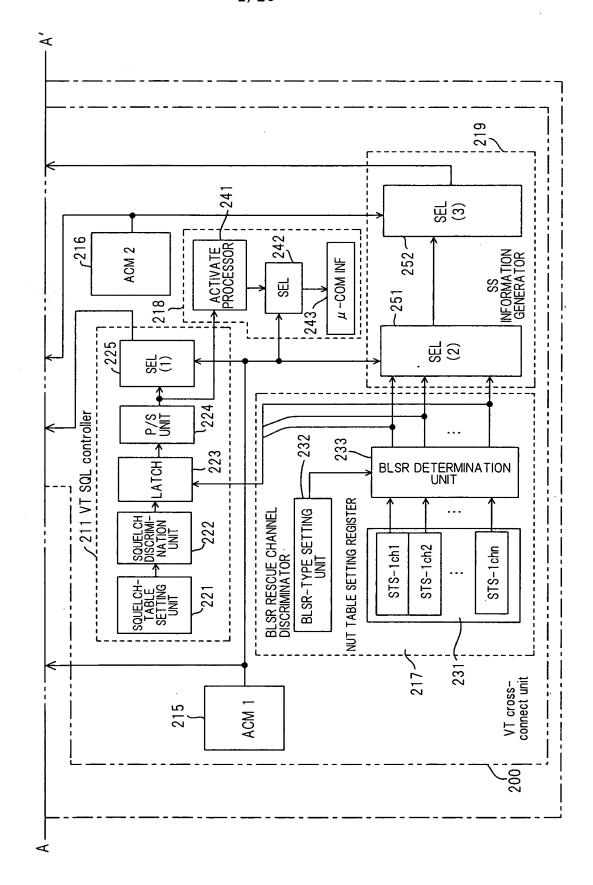


FIG. 1C



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FIG. 2

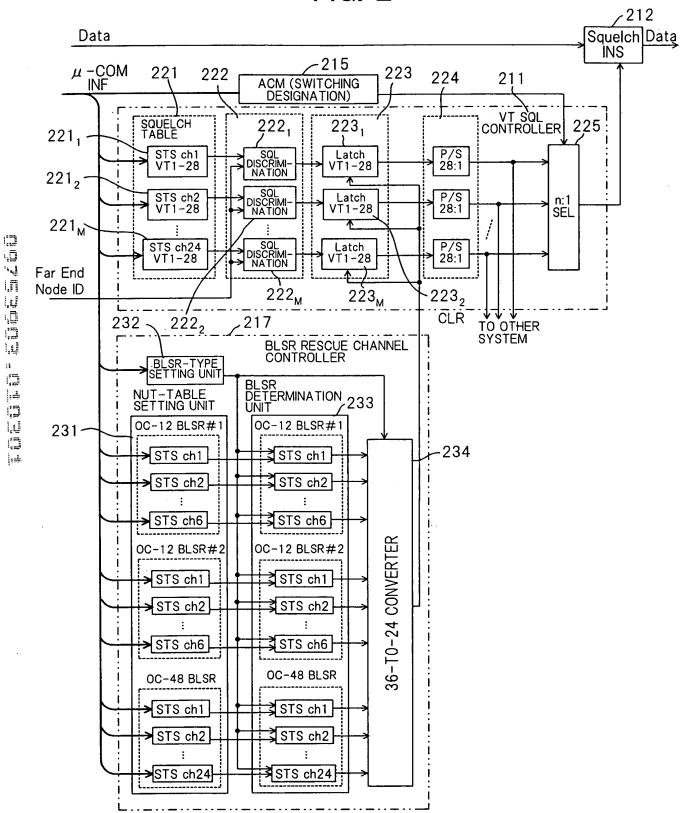


FIG. 3A

<CONFIGURATION FOR
OC-12 BLSR APPLICATION>

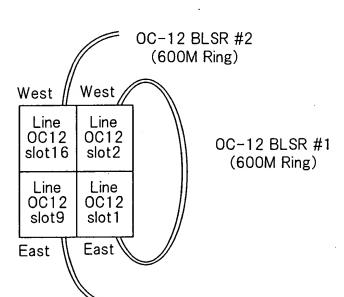


FIG. 3B

<CONFIGURATION FOR
OC-48 BLSR APPLICATION>

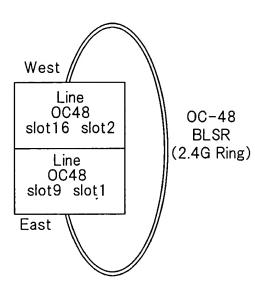
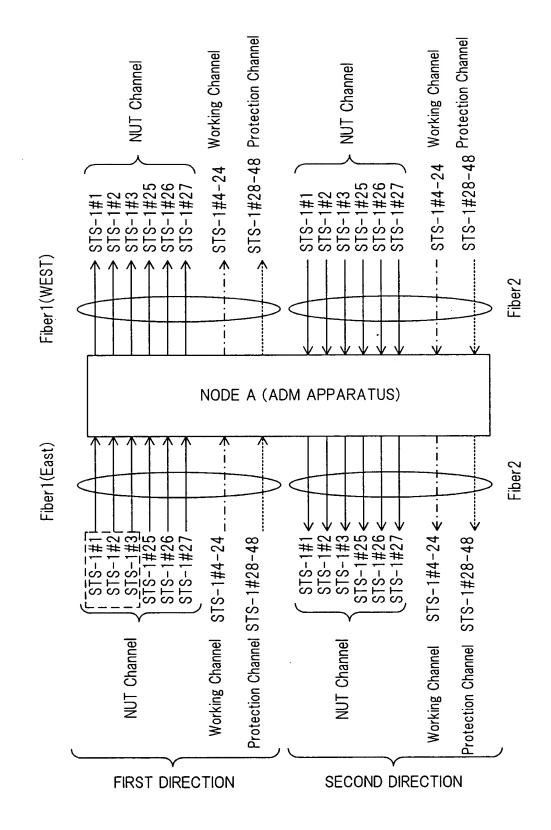


FIG. 4



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FIG. 5

OC-12 BLSR #1 D15 14 13 12 11 10 5 * * ch6 | ch5 | ch4 | ch3 | ch2 | ch1 OC-12 BLSR #2 D15 14 13 12 11 10 * * * * * * * ch6 ch5 ch4 ch3 ch2 * ch1 OC-48 BLSR D15 14 13 12 10 5 11 6 * * ch12 ch11 ch10 ch9 ch8 ch7 ch6 ch5 ch4 ch3 ch2 * * ch1 OC-48 BLSR D15 14 13 7 6 11 5 10 * ch24|ch23|ch22|ch21|ch20|ch19|ch18|ch17|ch16|ch15|ch14|ch13

"1"=NUT channel

"0"=not NUT channel

FIG. 6

BLSR Type			
OC12-2	OC12-1	OC-48	

OC-48: OC-48 BLSR DESIGNATION; "1": WHEN OC-48 BLSR; "0": NOT OC-48 BLSR OC-12-1: OC-12 BLSR #1 DESIGNATION; "1": WHEN OC-12 BLSR #1; "0": NOT OC-12 BLSR #1 OC-12-2: OC-12 BLSR #2 DESIGNATION; "1": WHEN OC-12 BLSR #2; "0": NOT OC-12 BLSR #2

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FIG. 7A

APPLICATION	NUMBER OF NUT CHANNEL SETTING REGISTERS ACCORDING TO PRIOR ART (N)	NUMBER OF NUT CHANNEL SETTING REGISTERS ACCORDING TO PRESENT INVENTION (M)
not BLSR		0
OC-12 BLSR	192	6
OC-48 BLSR		24

(WHEN MAXIMUM VT ACCESS PROCESSING CAPACITY OF APPARATUS IS 10 Gbps)

FIG. 7B

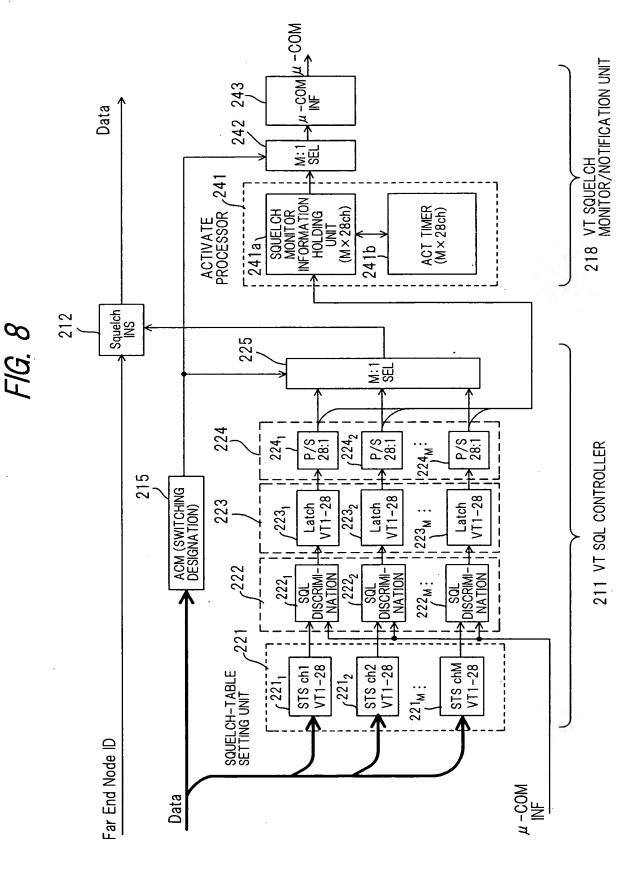
APPLICATION	NUMBER OF BLSR- TYPE SETTING REGISTERS ACCORDING TO PRIOR ART (N)	NUMBER OF BLSR- TYPE SETTING RÉGISTERS ACCORDING TO PRIOR ART (L)
WHEN OC-12 BLSR, OC-48 BLSR OR ITEM OTHER THAN BLSR CAN BE SELECTED	192	2

(WHEN MAXIMUM VT ACCESS PROCESSING CAPACITY OF APPARATUS IS 10 Gbps)

FIG. 7C

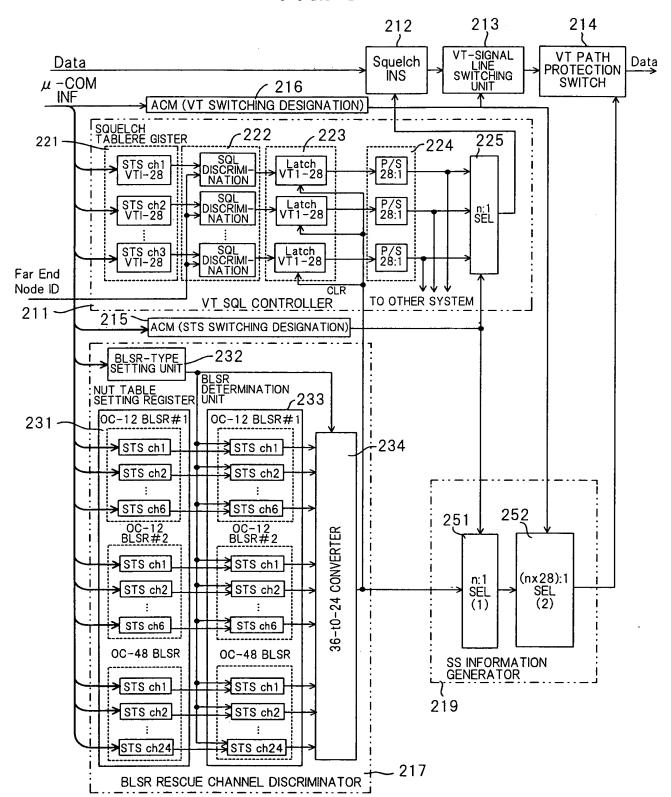
APPLICATION	NUMBER OF SQL ACTIVATE PROCESSING CHANNELS ACCORDING TO PRIOR ART (NxVT*)	NUMBER OF SQL ACTIVATE PROCESSING CHANNELS ACCORDING TO PRESENT INVENTION (MxVT*)
not BLSR		0
OC-12 BLSR	5376	168 (=6×28)
OC-48 BLSR		672 (=24×28)

(WHEN MAXIMUM VT ACCESS PROCESSING CAPACITY OF APPARATUS IS 10 Gbps)



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FIG. 9



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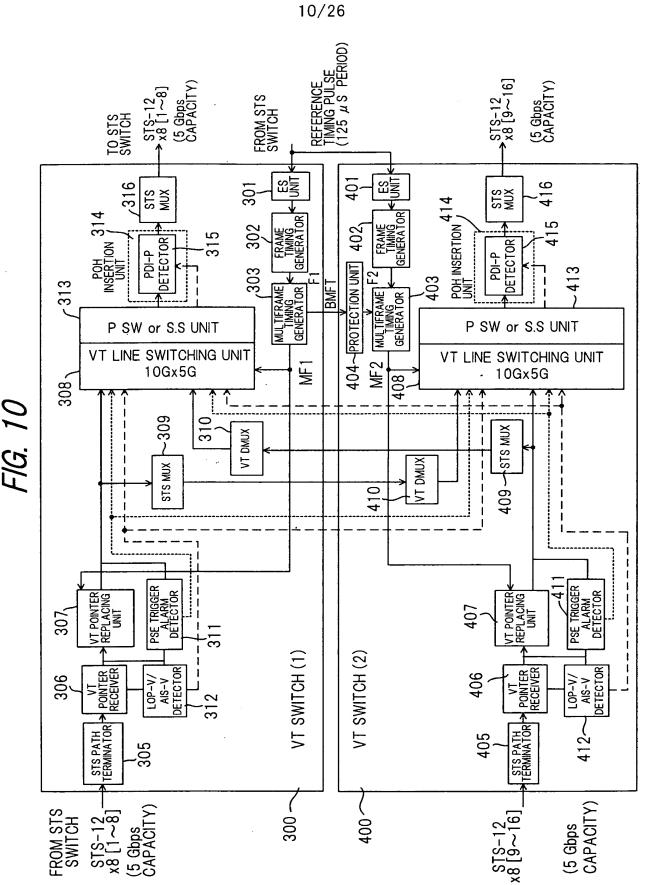


FIG 11

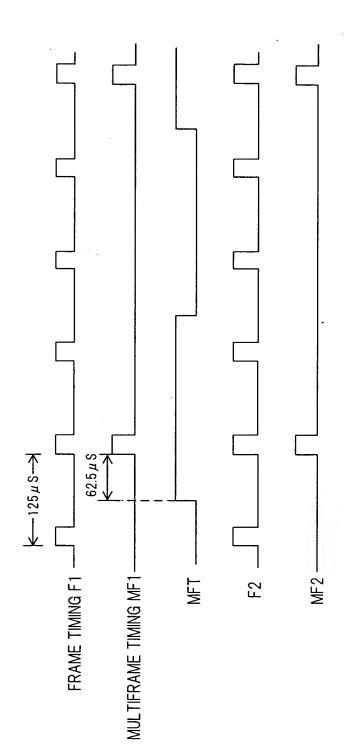


FIG. 12 PRIOR ART

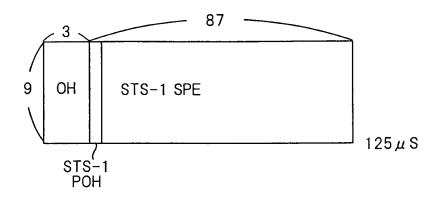


FIG. 13 PRIOR ART

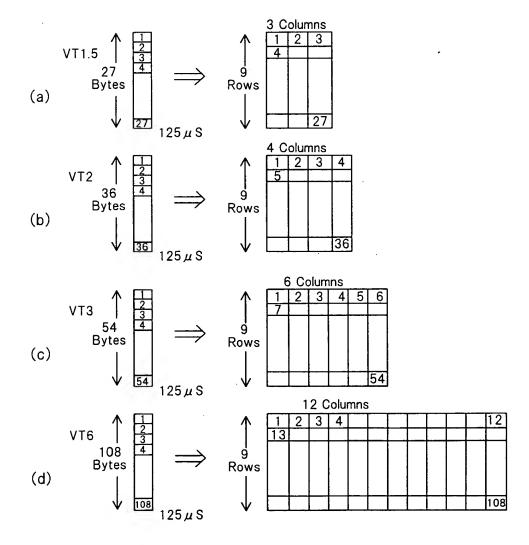


FIG. 14 PRIOR ART

---> STS-1 SPE Columns

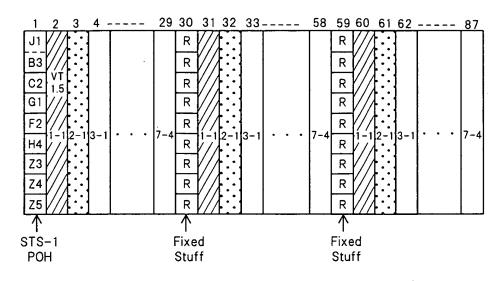


FIG. 15 PRIOR ART

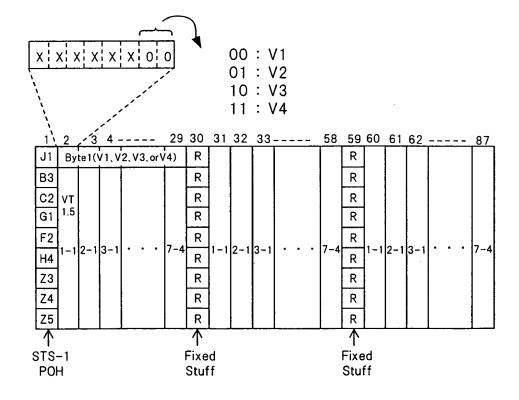
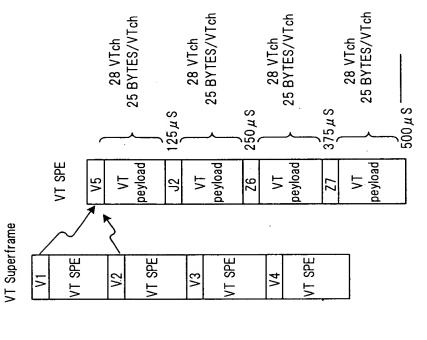


FIG. 16A PRIOR ART

28 VTch 26 BYTES/VTch 28 VTch 26 BYTES/VTch 28 VTch 26 BYTES/VTch 26 BYTES/VTch 28 VTch VT Superframe & Envelope Capacity 500 m S 125 µS 250 µS 375 µ Š STS-1 SPE VT SPE VT SPE VT SPE VT SPE ۸3 XXXXX10 XXXXX11 XXXXX01 **O**XXXXXXX

VT SPE & Payload Capacity

FIG. 16B PRIOR ART



VT peyload capacity: 100 (bytes/VT SPE)

VT Envelope Capacity: 104 (bytes/Superframe)

15/26 FIG. 17 PRIOR ART

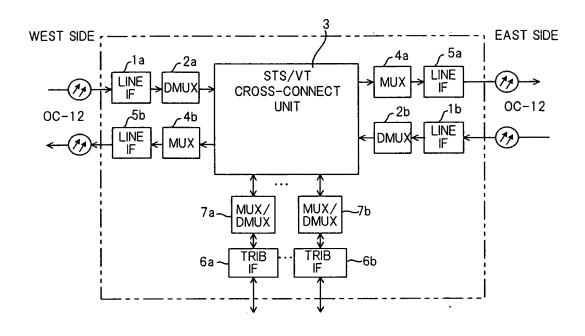
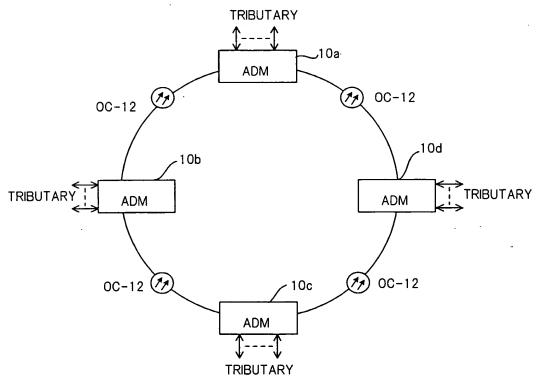


FIG. 18 PRIOR ART





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FIG. 19 PRIOR ART

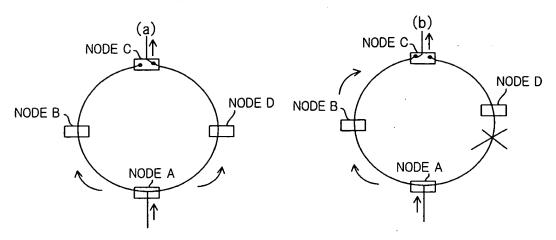


FIG. 20 PRIOR ART

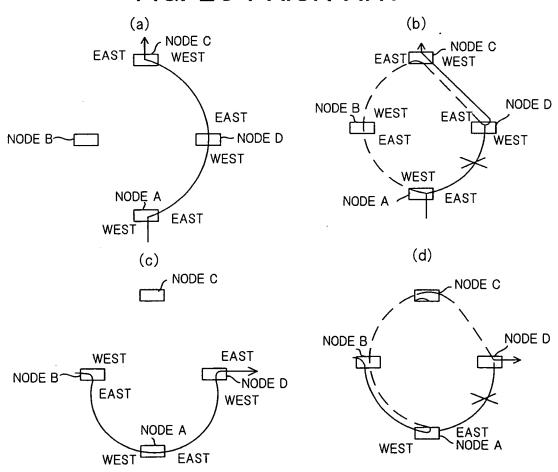


FIG. 21 PRIOR ART

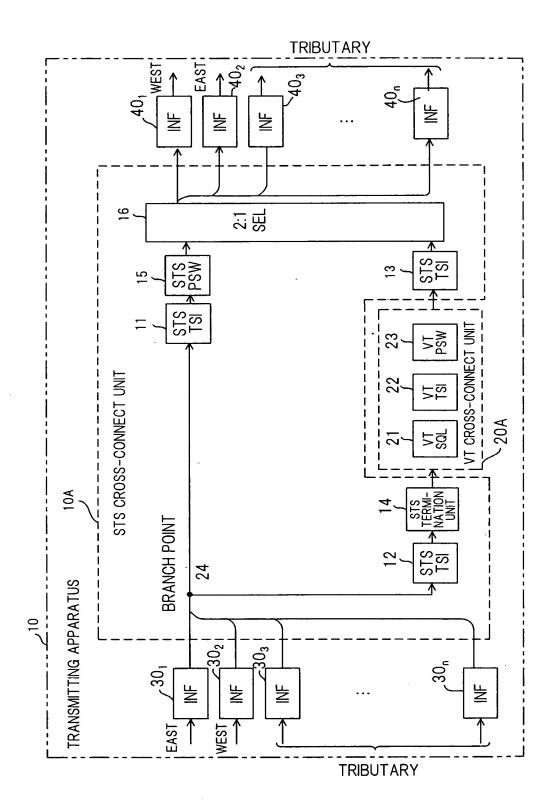


FIG. 22A PRIOR ART

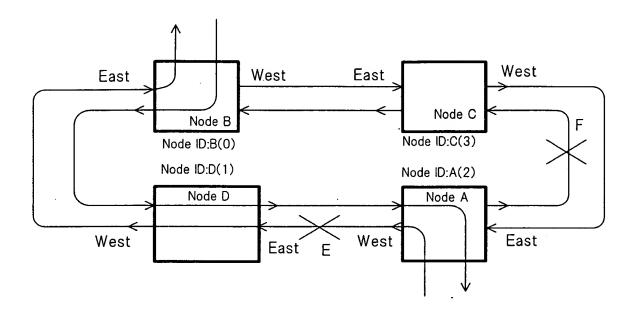
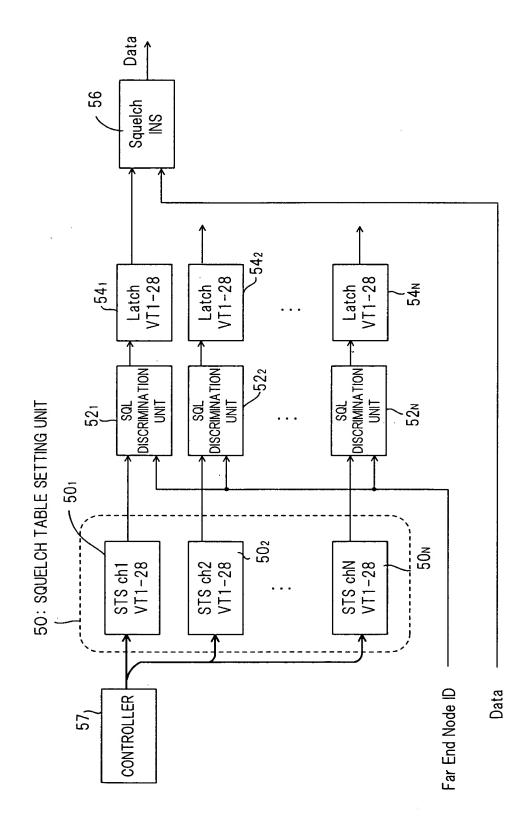


FIG. 22B PRIOR ART

Node B VT Squelch Table

East Side	West Side
2	0

FIG. 23 PRIOR ART



^{20/26} *FIG. 24 PRIOR ART*

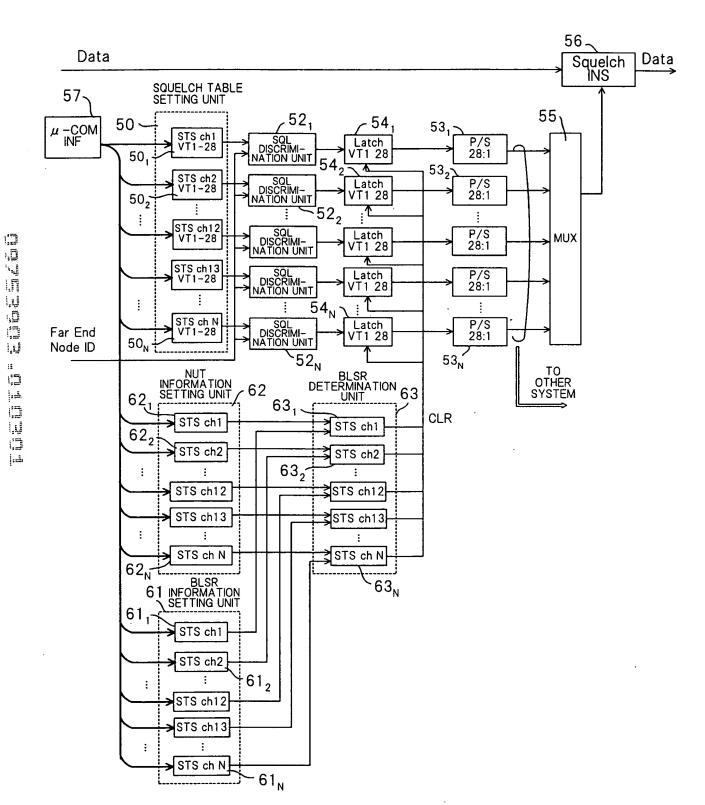
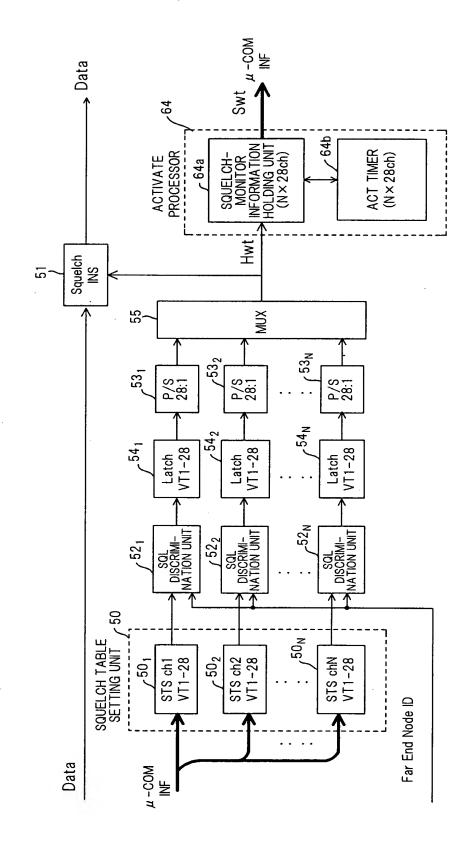


FIG. 25 PRIOR ART



22/26 FIG. 26 PRIOR ART

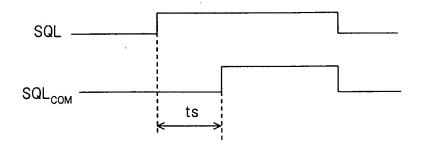


FIG. 27 PRIOR ART

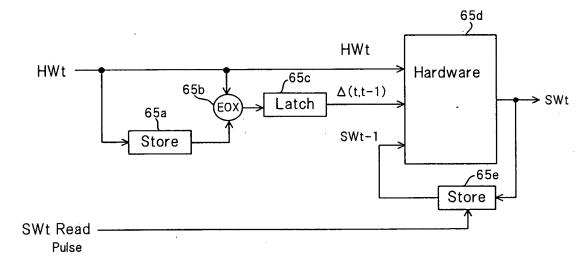


FIG. 28 PRIOR ART

SWt-1	∆(t,t−1)	HWt	SWt
0	0	0	0
0	0	1	1
0	1	0	1
. 0	1	1	1
1	0	0	0
1	0	1	1
1	1	0	0
1	1	1	0

23/26 FIG. 29 PRIOR ART

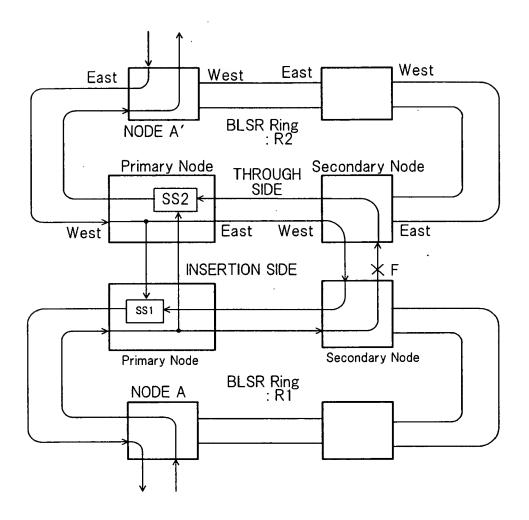
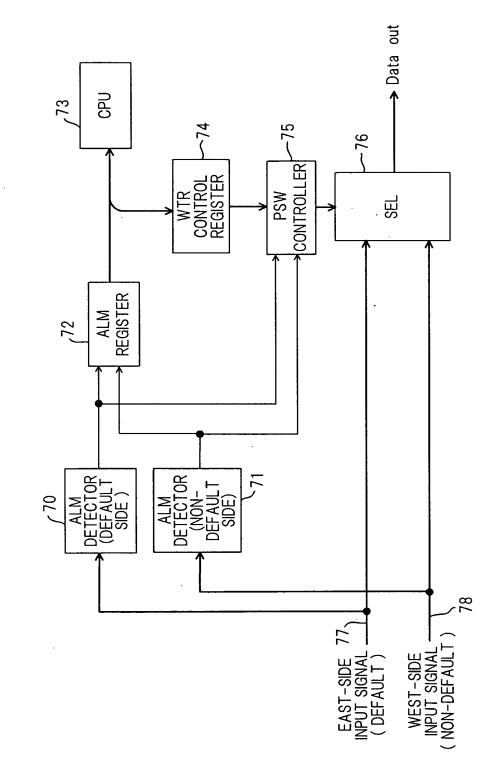
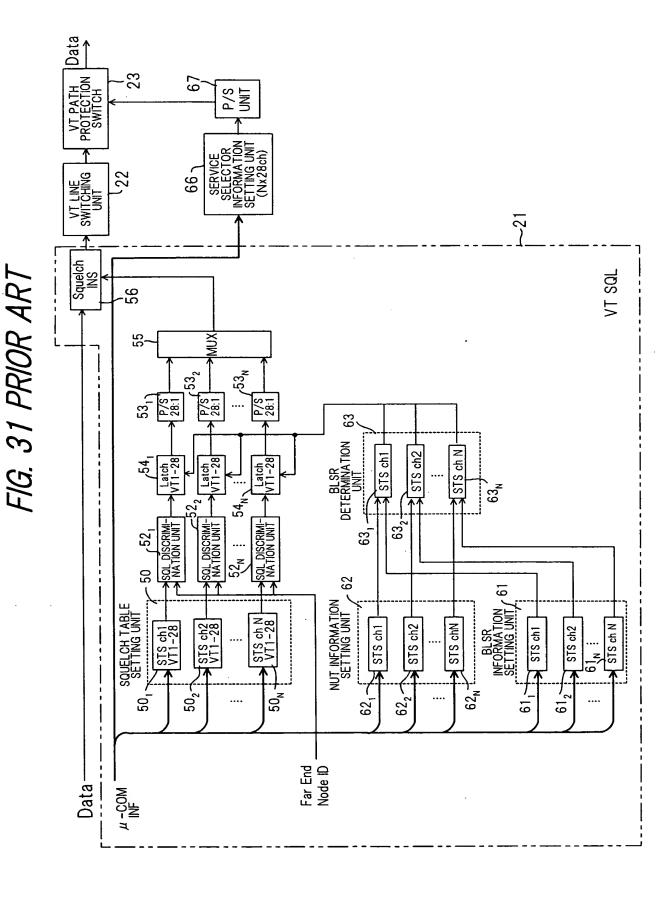
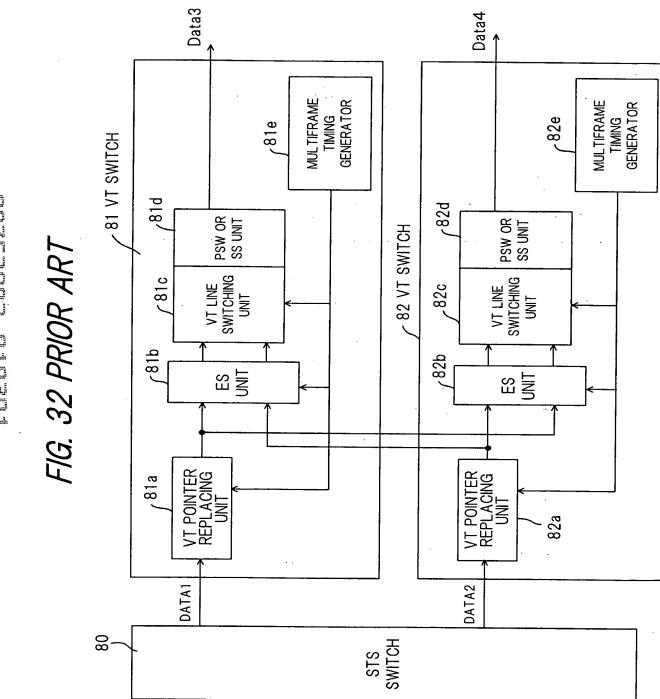


FIG. 30 PRIOR ART





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